

ABOUT THE INSTITUTION

Government College of Engineering, Srirangam, was started in the year 2013 with the notion of serving the students from marginalized family and rural areas across the state. It is the one among the state owned AICTE approved institutions providing quality Engineering Education. The Anna university affiliated institution has Six Courses in various Engineering Disciplines. The Institution comprises of faculty with a determination to educate the students, an intellectual student community, well supporting administrative, technical and non-technical staff is striving to progress in producing reputed engineers to serve the nation.

ABOUT THE DEPARTMENT

Department of Electronics and Communication Engineering was started in the year 2013. The department with its highly qualified faculty, cozy infrastructure and modernized high-tech laboratory facilities strives to empower the students with the theoretical and as well as the practical knowledge in all the domains in order to face the competitive world with confidence, courage and success. Presently the department has a total strength of 163 students. It has established text book facilities in the library and computing facilities like computer simulation Lab and interactive facility like smart class room. Laboratories such as VLSI & Signal Processing, Electronic Devices, Optical, Embedded, Networking, μP & μC and Anechoic antenna chamber were established to cater the needs of the aspiring students.

ABOUT THE WORKSHOP

The "Analog IC Design using CADENCE EDA Tools" workshop is a hands-on training program designed to equip students with practical knowledge in the field of Analog Integrated Circuit (IC) design. This workshop provides an in-depth understanding of analog design principles, methodologies, and implementation using **CADENCE EDA tools**.

Participants will be guided through the complete design flow, including schematic design, simulation, layout, DRC/LVS checks, and post-layout simulation. Emphasis will be placed on real-time circuit design with examples.

This workshop is ideal for those aiming to pursue careers in VLSI design, semiconductor industries, or advanced academic research. Through expert-led sessions and practical labs, attendees will gain valuable insights and experience that bridge the gap between theory and practices.

TOPICS TO BE DISCUSSED

- Introduction to Analog IC Design
- Overview of CADENCE EDA Tools
- CMOS Technology and Device Modeling
- Analog Building Blocks
- Design and Simulation using Virtuoso
- Layout Design and Physical Verification
- Post-Layout Simulation and Optimization
- Case Studies and Mini Projects
- Best Practices and Industry Trends

TWO - DAY NATIONAL LEVEL TECHNICAL WORKSHOP ON “ANALOG IC DESIGN USING CADENCE EDA TOOL”

22-23 August 2025

Organized by



**DEPARTMENT OF
ELECTRONICS & COMMUNICATION
ENGINEERING**

**Government College of
Engineering Srirangam**

(Affiliated to Anna University)

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